PATENT APPLICATION

In re application of

Docket No: Q83757

Tatsuo TSUJITA

Group Art Unit: 2819

Appln. No.: 10/508,795

Examiner: Brian K. Young

Confirmation No.: 3903

Patent No.: 7,049,994

Filed: September 21, 2004

Issue Date: May 23, 2006

For:

SEMICONDUCTOR INTEGRATED CIRCUIT

REQUEST FOR CERTIFICATE OF CORRECTION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTN: Certificate of Correction Branch

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the provisions of 37 C.F.R. § 1.322, please enter the attached Certificate of Correction.

Since the errors noted are believed to be the fault of the Patent and Trademark Office, we are not enclosing the \$100.00 Certificate of Correction fee. If it is found to be to the contrary, please charge our Deposit Account No. 19-4880.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

Respectfully submitted,

Registration No. 25,426

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WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: September 18, 2006

SEP 2:0 2006

of Correction

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO:

7,049,994

DATED:

May 23, 2006

INVENTOR(S):

Tatsuo TSUJITA

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 10, lines 3 and 4, please correct the word "seciuentially" to seciuentially.

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CUSTOMER NUMBER

PATENT NO. 7,049,994

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1